

Refine Search

Search Results -

| Term | Documents |
|-------------------------|-----------|
| (18 AND 8).PGPB,USPT. | 52 |
| (L8 AND L18).PGPB,USPT. | 52 |

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L29

Refine Search

Recall Text



Clear

Interrupt

Search History

 DATE: Wednesday, May 10, 2006 [Printable Copy](#) [Create Case](#)

Set
Name **Query**
 side by
 side

Hit
Count **Set**
 Name
 result
 set

DB=PGPB,USPT; PLUR=YES; OP=OR

| | | | |
|------------|---|----|------------|
| <u>L29</u> | 18 and 118 | 52 | <u>L29</u> |
| <u>L28</u> | 18 and 117 | 70 | <u>L28</u> |
| <u>L27</u> | 18 and 116 | 4 | <u>L27</u> |
| <u>L26</u> | 18 and 115 | 43 | <u>L26</u> |
| <u>L25</u> | roll\$3 and 11 | 0 | <u>L25</u> |
| <u>L24</u> | 11 and (fectch\$4 or counter or prefetch\$5 or dispatch\$5 or iss\$5 or pc) | 0 | <u>L24</u> |
| <u>L23</u> | 11 and (fectch\$4 or counter) | 0 | <u>L23</u> |
| <u>L22</u> | 17 and 118 | 3 | <u>L22</u> |
| <u>L21</u> | 17 and 117 | 0 | <u>L21</u> |
| <u>L20</u> | 17 and 116 | 2 | <u>L20</u> |
| <u>L19</u> | 17 and 115 | 2 | <u>L19</u> |

| | | | |
|---|--|-------|------------|
| <u>L18</u> | (716/15-19)[CCLS] | 3188 | <u>L18</u> |
| <u>L17</u> | (326/41,114,125)[CCLS] | 1557 | <u>L17</u> |
| <u>L16</u> | (712/220-225)[CCLS] | 1683 | <u>L16</u> |
| <u>L15</u> | (712/2-300)[CCLS] | 12096 | <u>L15</u> |
| <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> | | | |
| <u>L14</u> | (program\$6 or instruction\$1 or compil\$7) and l1 | 1 | <u>L14</u> |
| <u>L13</u> | (program\$6 or instruction\$1 or compil\$7) and l1 | 1 | <u>L13</u> |
| <u>L12</u> | read\$3 and l10 | 1 | <u>L12</u> |
| <u>L11</u> | L10 and (protect\$3 or prevent\$5 or inhibit\$3 or permi\$5) | 2 | <u>L11</u> |
| <u>L10</u> | 5271019.pn. | 2 | <u>L10</u> |
| <u>L9</u> | state\$1 near12 (modif\$5 or chang\$5 or updat\$5) near12 (ram or random\$3 near3 memor\$4) near15 bit\$1 near15 device\$1 | 10 | <u>L9</u> |
| <u>L8</u> | short\$3 near8 ("AND" or "OR" or logic\$3 or circuit\$3) near25 device\$1 | 43340 | <u>L8</u> |
| <u>L7</u> | short\$3 near8 ("AND" or "OR" or logic\$3 or circuit\$3) near35 boolean | 46 | <u>L7</u> |
| <u>L6</u> | L5 and boolean | 4 | <u>L6</u> |
| <u>L5</u> | state\$1 near12 (modif\$5 or chang\$5 or updat\$5) near12 (ram or random\$3 near3 memor\$4) near15 bit\$1 | 178 | <u>L5</u> |
| <u>L4</u> | L3 and encod\$5 | 1 | <u>L4</u> |
| <u>L3</u> | L1 and (beg\$6 or initial\$7) | 2 | <u>L3</u> |
| <u>L2</u> | L1 and (beg\$6 or initial\$7) | 2 | <u>L2</u> |
| <u>L1</u> | 5682519.pn. | 2 | <u>L1</u> |

END OF SEARCH HISTORY